V115-TDC (4-2005)

TRANSMITTAL

10/713,374 **Application Number** 11/13/2003 Filing Date Rajen Chanchani First Named Inventor **FORM** 2826 Art Unit Alexander O. Williams **Examiner Name** (to be used for all correspondence after initial filing) SD7151/S98758 Attorney Docket No. Total Number of Pages in This Submission

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Chanchani

Group:

2826

Serial No.:

10/713,374

Paper No.:

Filed:

11/13/2003

Examiner:

Alexander O. Williams

For:

Heterogeneously Integrated System-on-a-Chip

RESPONSE TO RESTRICTION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants have carefully reviewed the Office Action mailed 9/1/05, submit the following in complete response thereto, and request reconsideration of the claims presented.

REMARKS

In further support of the Claims presented, Applicants provide the following discussion.

The Office issued a restriction requirement:

- I. Claims 1-12, drawn to a micro-system on a chip.
- II. Claims 13-16, drawn to a method for fabricating a plurality of microsystems on a chip.

ELECTION

Applicants elect the **Invention I**, without traverse. Claims 1-12 all read on and comprehend a microsystem on a chip.

CONCLUSION

Applicant has responded to each and every requirement and urge that the claims as presented are now in condition for allowance. Applicants request expeditious processing to issuance.

Respectfully submitted,

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CERTIFICATION UNDER 37 CFR 1.8

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